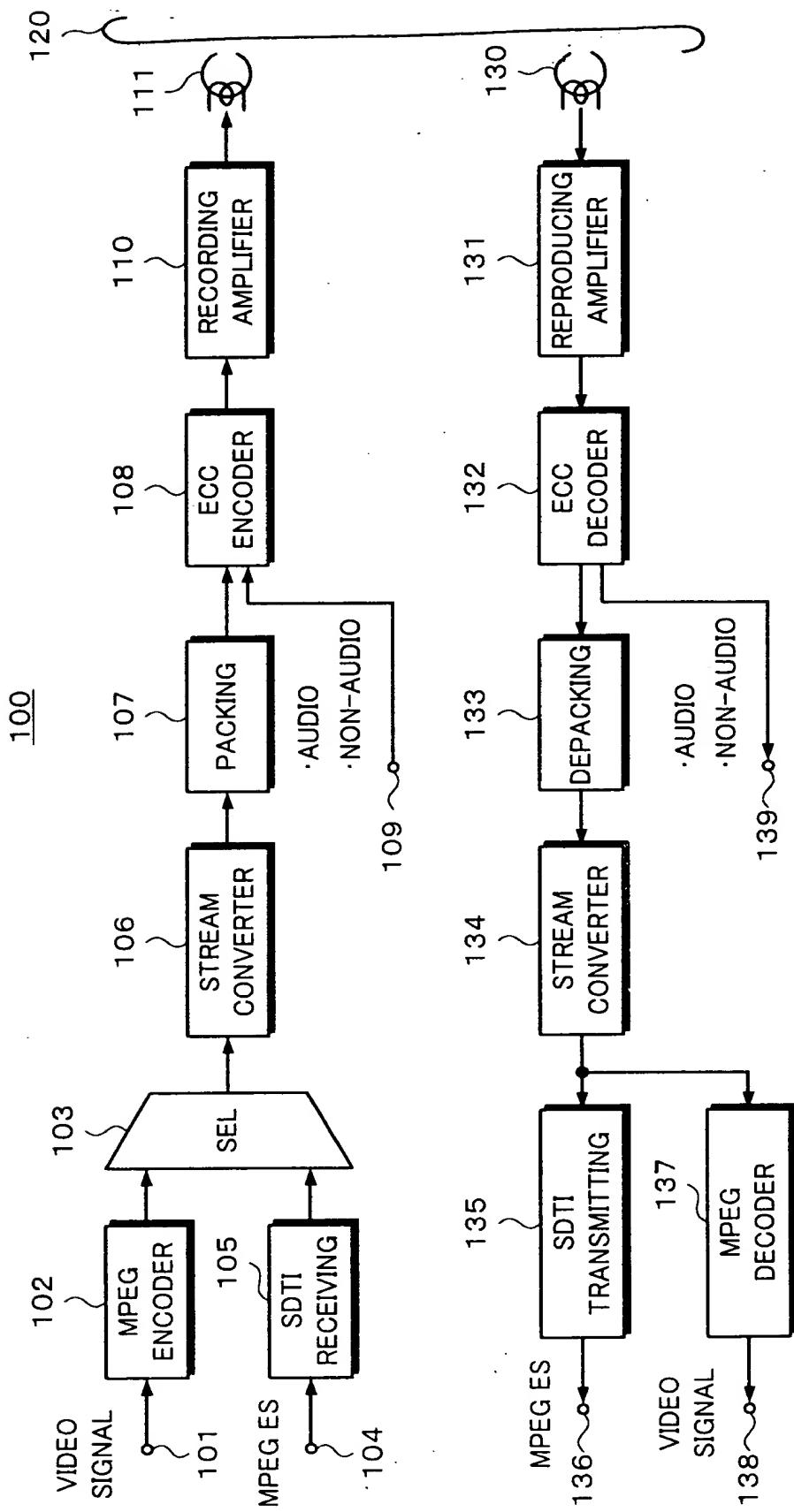


Fig. 1

09/581801

Fig. 2

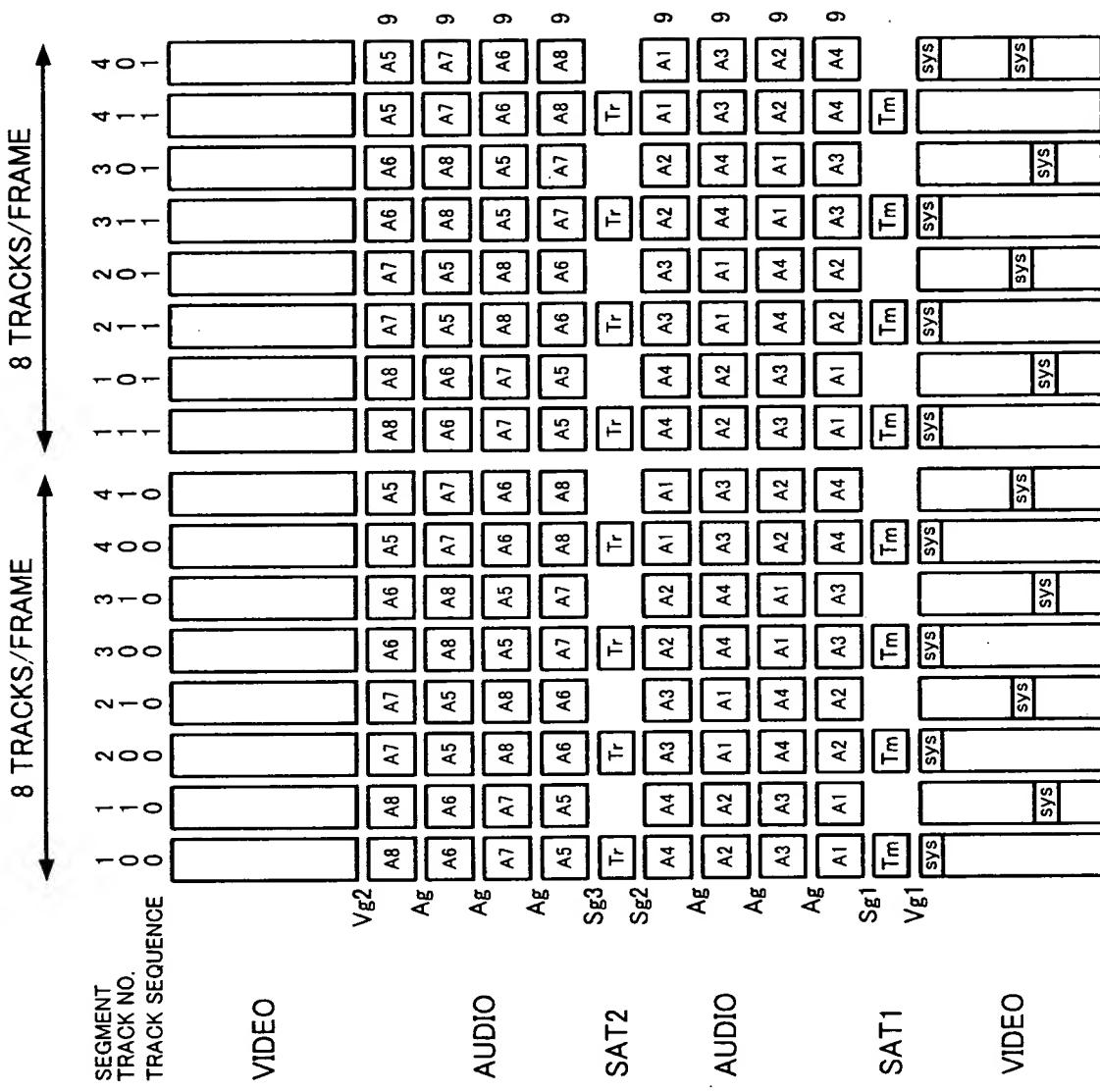
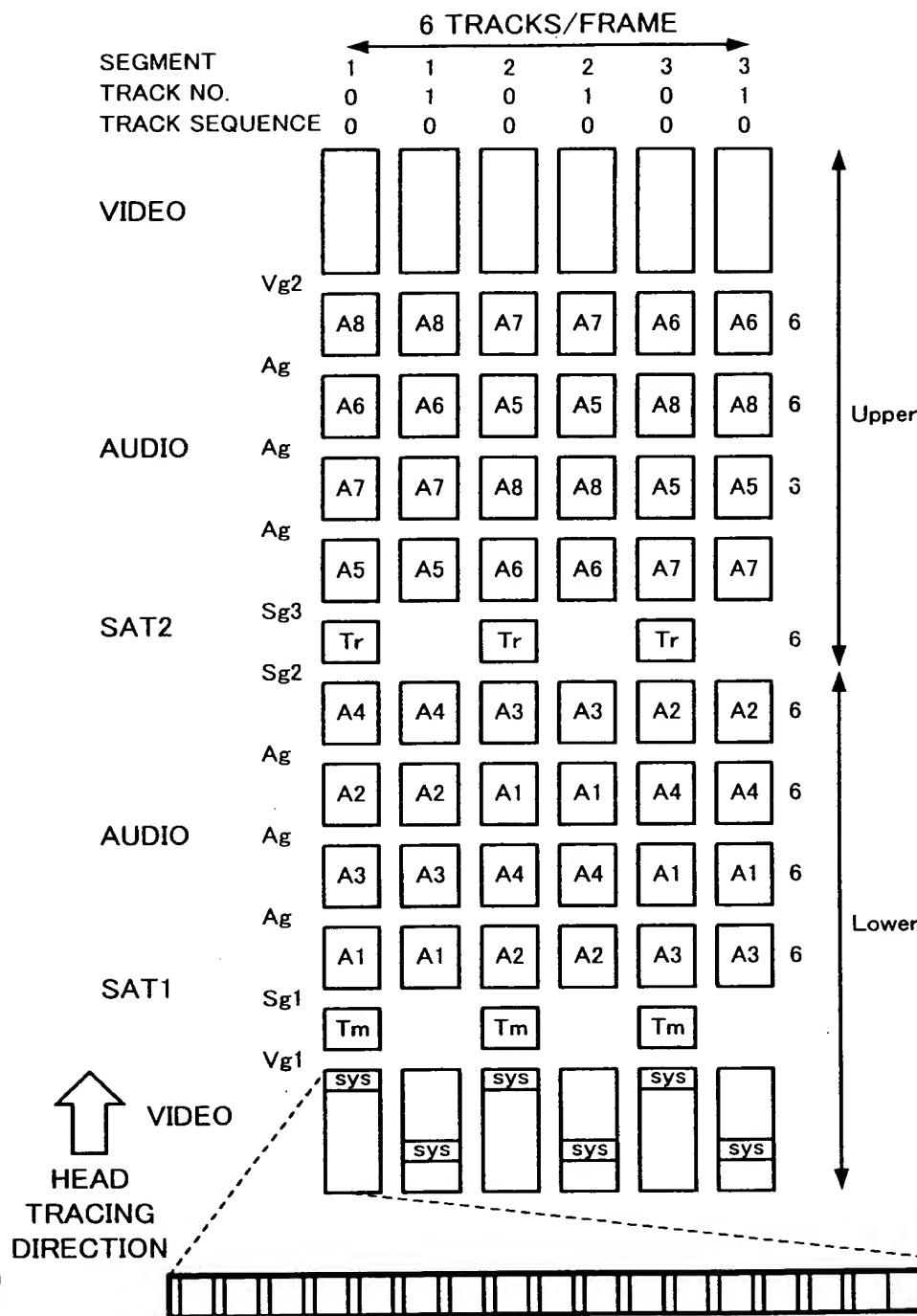


Fig. 3A**Fig. 3B****Fig. 3C**

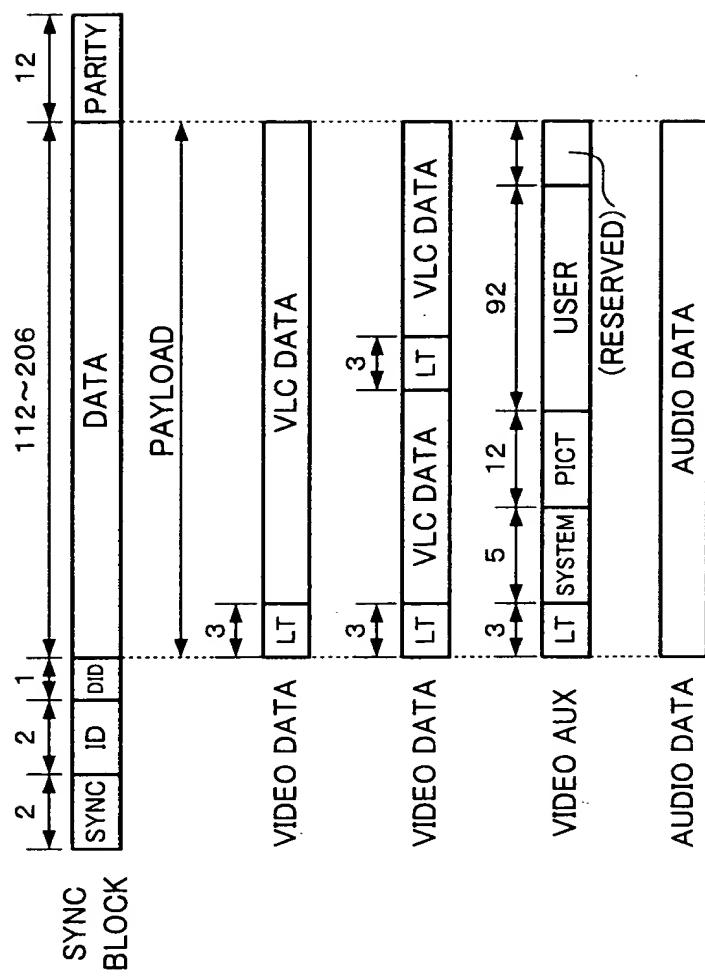


Fig. 4A

Fig. 4B

Fig. 4C

Fig. 4D

Fig. 4E

Fig. 5A

MSB	ID0	ID1
7	SYNC ID7	Upper/Lower
6	SYNC ID6	(Reservvel)
5	SYNC ID5	SEG NB3
4	SYNC ID4	SEG NB2
3	SYNC ID3	SEG NB1
2	SYNC ID2	SEG NB0
1	SYNC ID1	TRACK
0	SYNC ID0	VIDEO/AUDIO

LSB

Fig. 5B Fig. 5C

DID(VIDEO)	DID(AUDIO)
(Reservvel)	(Reservvel)
PAYOUT MD1	DATA/AUDIO
PAYOUT MD0	5F Seg2
2MB/1MB	5F Seg1
Vouter	5F Seg0

Fig. 6

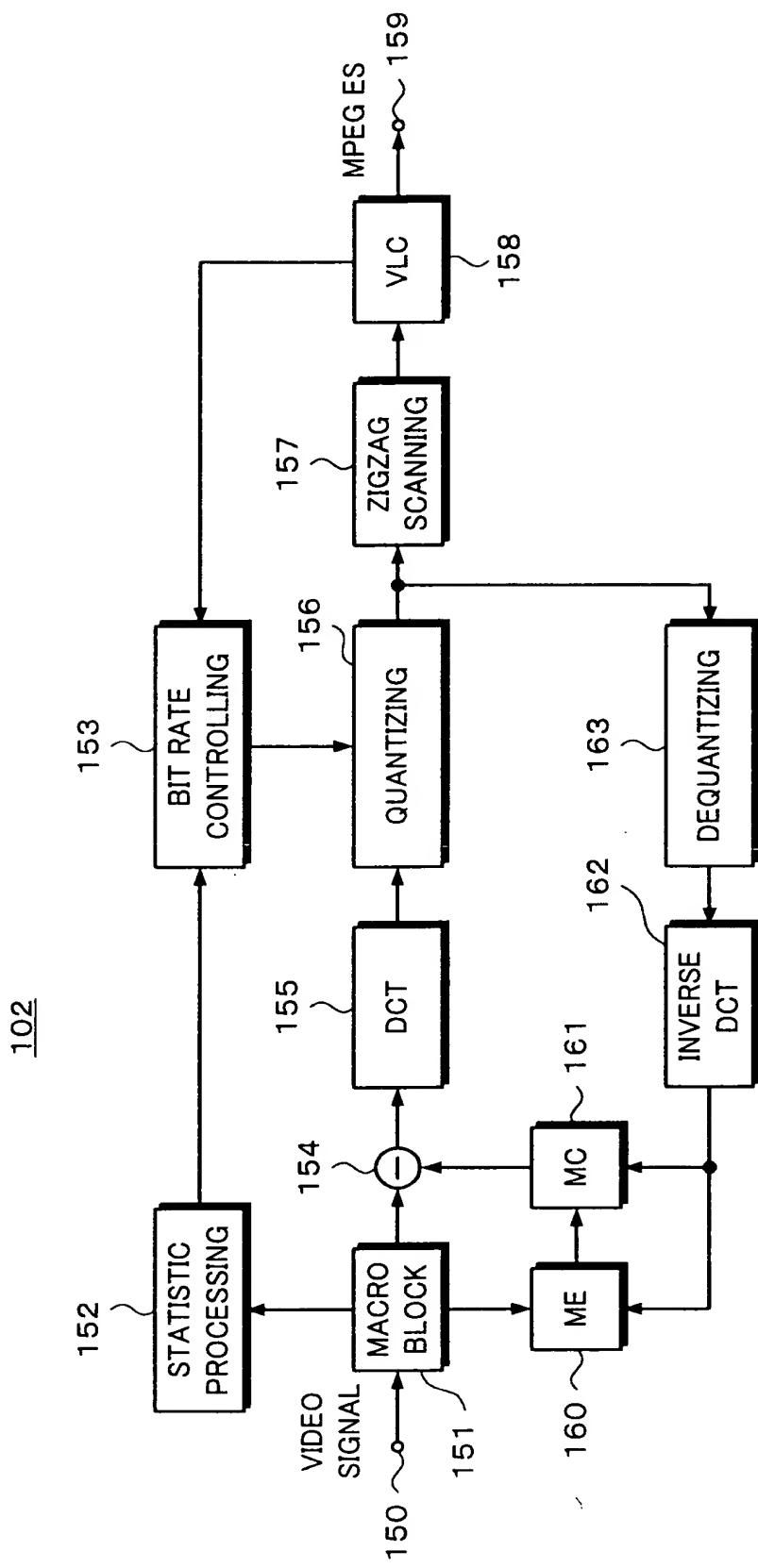


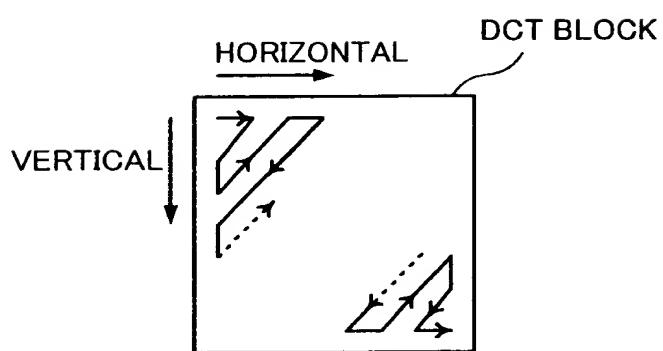
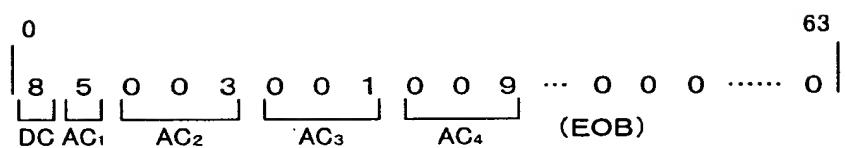
Fig. 7A*Fig. 7B*

Fig. 8A

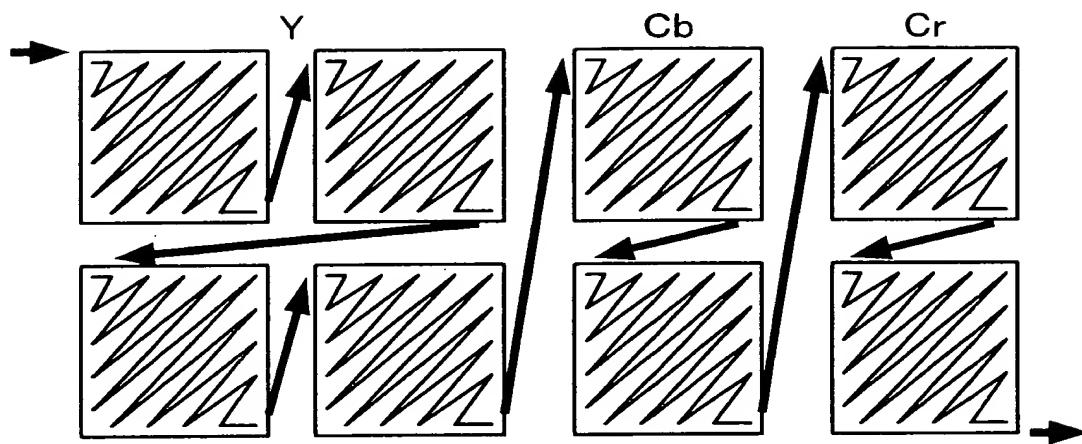


Fig. 8B

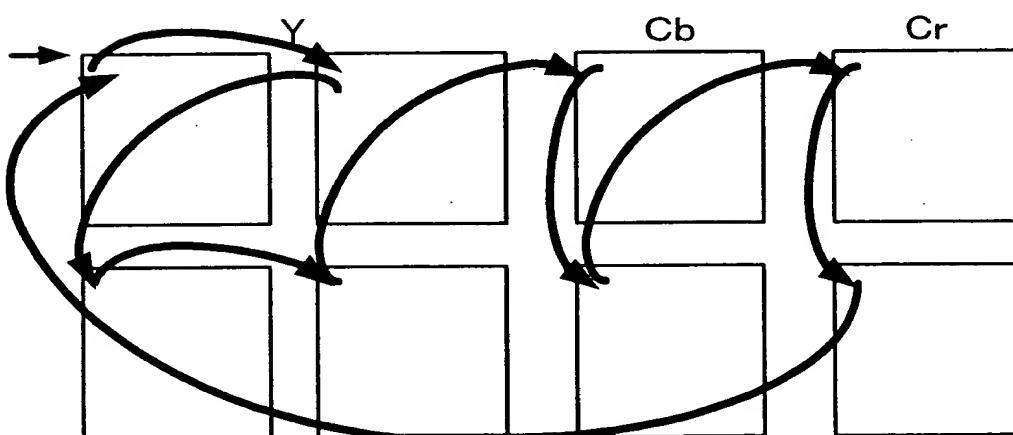


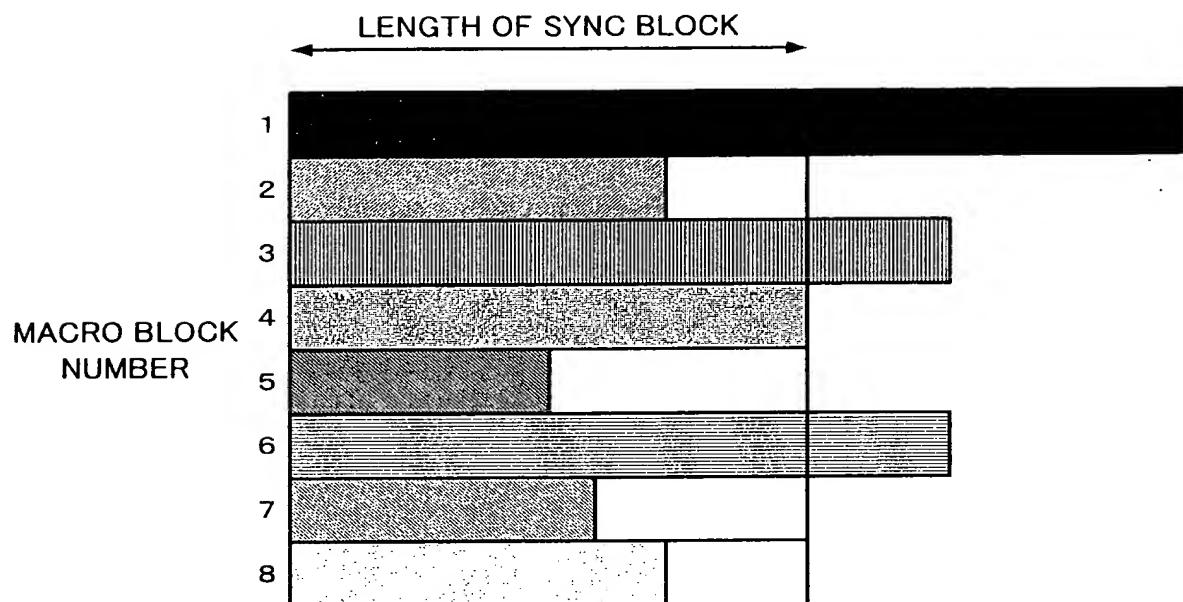
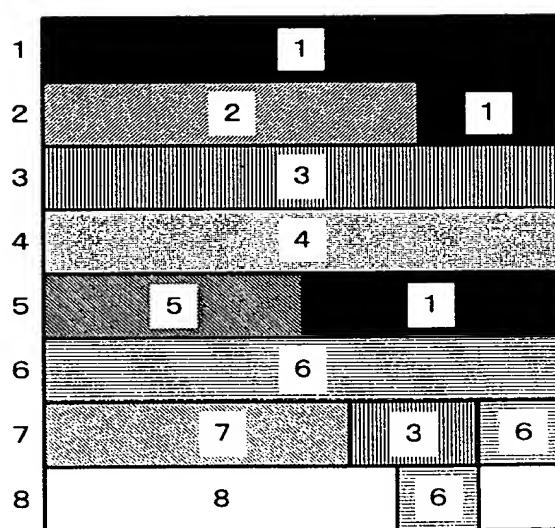
Fig. 9A*Fig. 9B*

Fig. 10

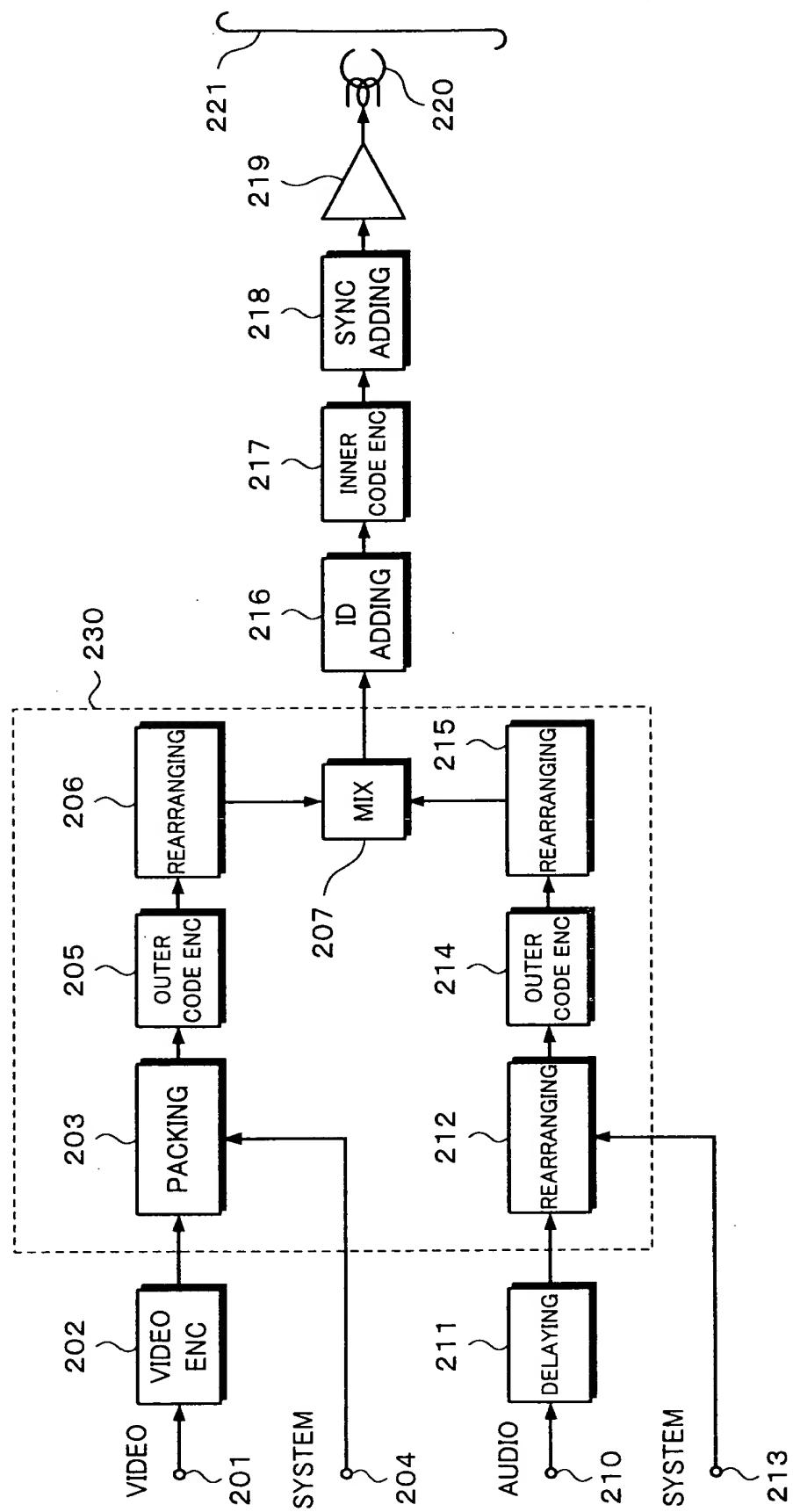


Fig. 11

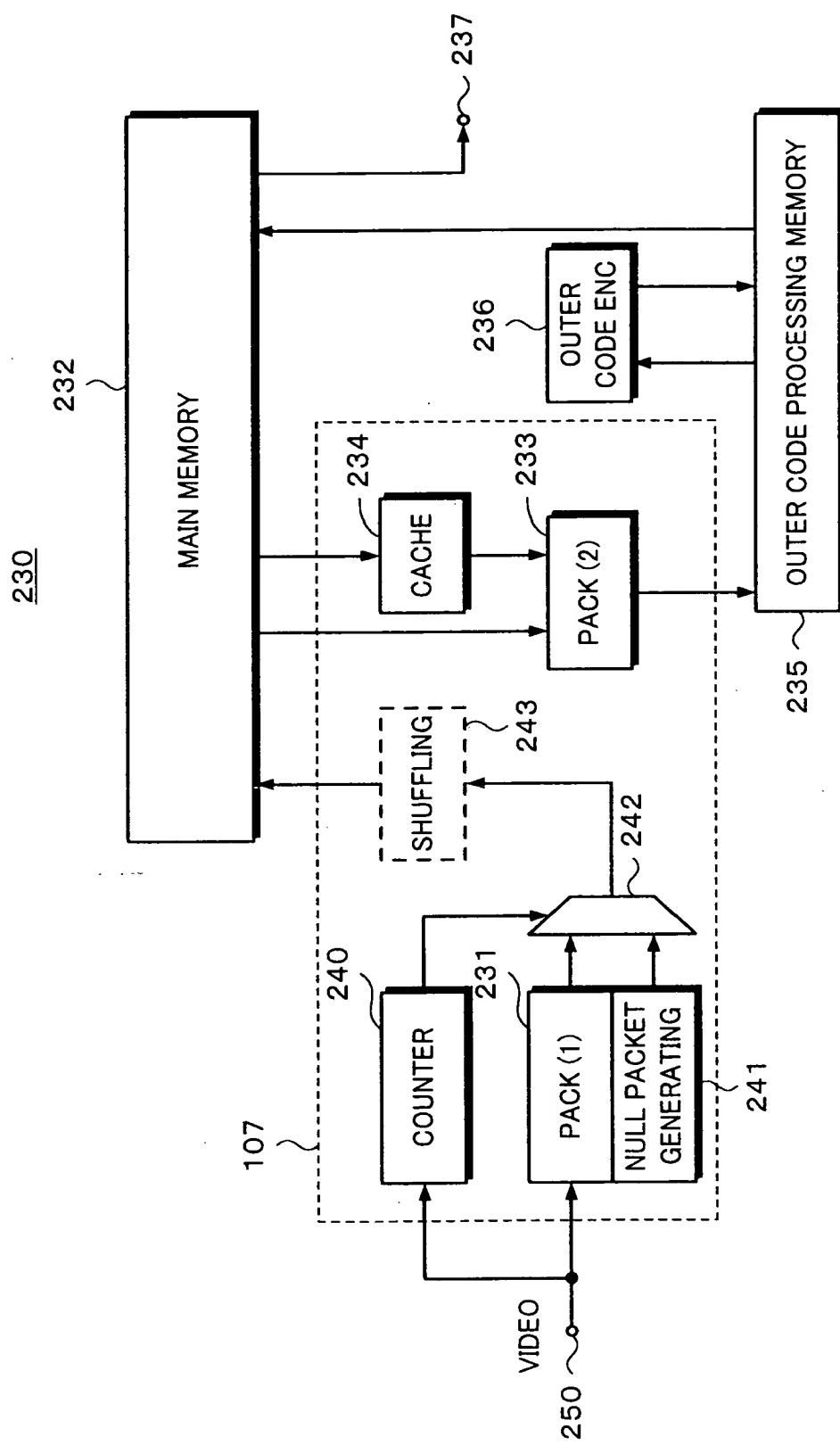
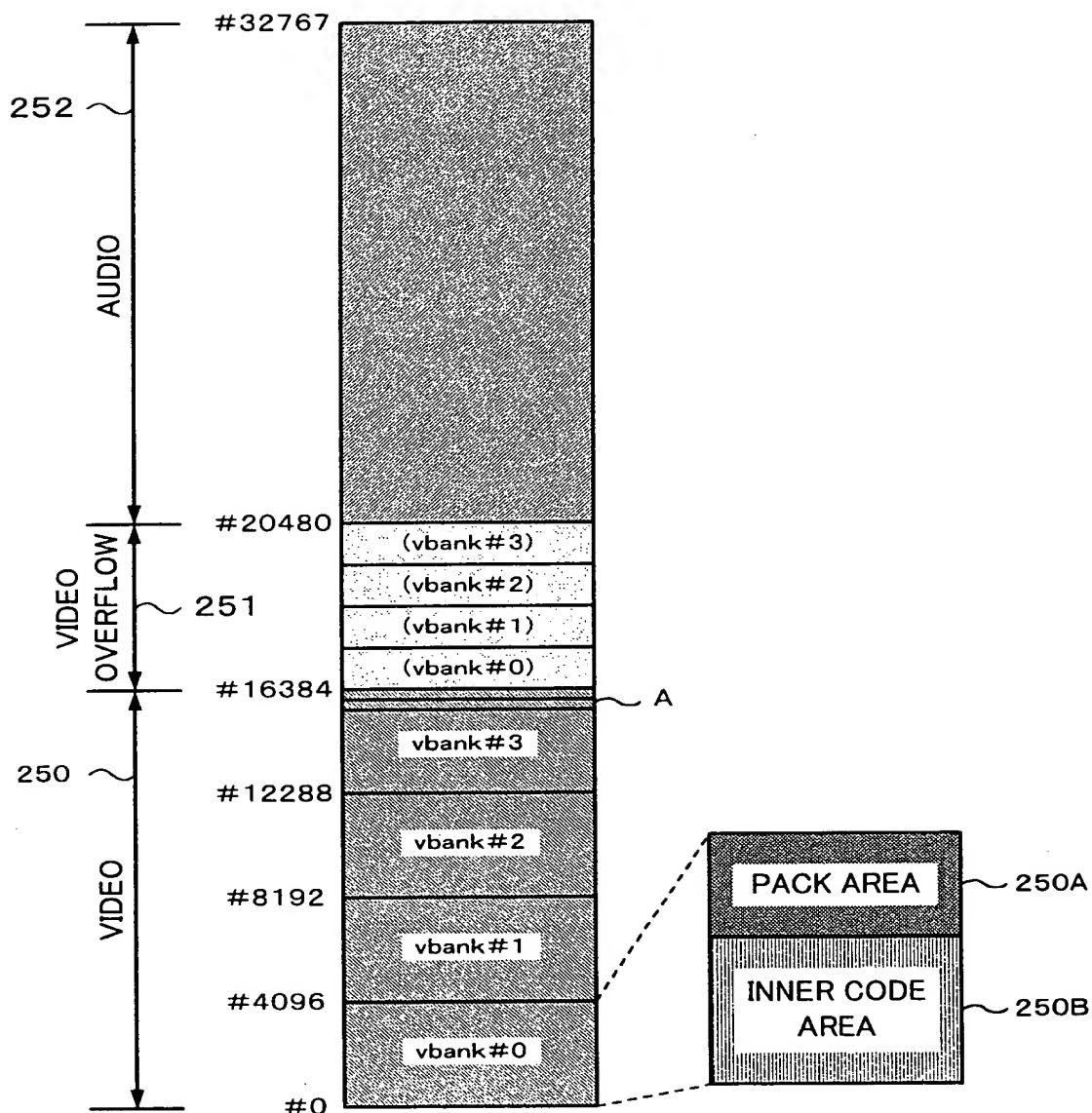


Fig. 12

232

09/581801

Fig. 13A

LENGTH	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
MB0	"15"	0-1	0-2	0-3	0-4	0-5	0-6	0-7	0-8	0-a	0-b	0-c	0-d	0-e	0-f	0-g
MB1	"13"	1-1	1-2	1-3	1-4	1-5	1-6	1-7	1-8	1-a	1-b	1-c	1-d	1-e		
MB2	"3"	2-1	2-2	2-3												
MB3	"1"	3-1														
MB4	"7"	4-1	4-2	4-3	4-4	4-5	4-6	4-7								
MB5	"11"	5-1	5-2	5-3	5-4	5-5	5-6	5-7	5-8	5-a	5-b	5-c				
MB6	"15"	6-1	6-2	6-3	6-4	6-5	6-6	6-7	6-8	6-a	6-b	6-c	6-d	6-e	6-f	6-g
MB7	"8"	7-1	7-2	7-3	7-4	7-5	7-6	7-7	7-8							
MB8	"13"	8-1	8-2	8-3	8-4	8-5	8-6	8-7	8-8	8-a	8-b	8-c	8-d	8-e		
MB9	"5"	9-1	9-2	9-3	9-4	9-5										
NULL1	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL2	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL3	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL4	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL5	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"

Fig. 13B

LENGTH	1	2	3	4	5	6	7	8
MB0	"15"	0-1	0-2	0-3	0-4	0-5	0-6	0-7
MB1	"13"	1-1	1-2	1-3	1-4	1-5	1-6	1-7
MB2	"3"	2-1	2-2	2-3				
MB3	"1"	3-1						
MB4	"7"	4-1	4-2	4-3	4-4	4-5	4-6	4-7
MB5	"11"	5-1	5-2	5-3	5-4	5-5	5-6	5-7
MB6	"15"	6-1	6-2	6-3	6-4	6-5	6-6	6-7
MB7	"8"	7-1	7-2	7-3	7-4	7-5	7-6	7-7
MB8	"13"	8-1	8-2	8-3	8-4	8-5	8-6	8-7
MB9	"5"	9-1	9-2	9-3	9-4	9-5		
NULL1	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL2	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL3	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL4	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL5	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"

Fig. 13C

0-a	0-b	0-c	0-d	0-e	0-f	0-g	1-a	1-b	1-c	1-d	1-e	5-a	5-b	5-c	6-a	6-b	6-c	6-d	6-e	6-f	6-g	8-a	8-b	8-c	8-d	8-e
-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----	-----

Fig. 14A

LENGTH	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
MB0	"15"	0-1	0-2	0-3	0-4	0-5	0-6	0-7	0-8	0-a	0-b	0-c	0-d	0-e	0-f	0-g
MB1	"13"	1-1	1-2	1-3	1-4	1-5	1-6	1-7	1-8	1-a	1-b	1-c	1-d	1-e		
MB2	"3"	2-1	2-2	2-3												
MB3	"1"	3-1														
MB4	"7"	4-1	4-2	4-3	4-4	4-5	4-6	4-7								
MB5	"11"	5-1	5-2	5-3	5-4	5-5	5-6	5-7	5-8	5-a	5-b	5-c				
MB6	"15"	6-1	6-2	6-3	6-4	6-5	6-6	6-7	6-8	6-a	6-b	6-c	6-d	6-e	6-f	6-g
MB7	"8"	7-1	7-2	7-3	7-4	7-5	7-6	7-7	7-8							
MB8	"13"	8-1	8-2	8-3	8-4	8-5	8-6	8-7	8-8	8-a	8-b	8-c	8-d	8-e		
MB9	"5"	9-1	9-2	9-3	9-4	9-5										
NULL1	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL2	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"01"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL3	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL4	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"
NULL5	"0"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"	"00"

09/581801

230: PACKING PORTION
231: PACK (1) CIRCUIT
232: MAIN MEMORY
233: PACK (2) CIRCUIT
234: CACHE
235: OUTER CODE PROCESSING MEMORY
236: OUTER CODE ENCODER
240: COUNTER
241: NULL PACKET GENERATING CIRCUIT
242: SWITCH CIRCUIT

09534834 - 06345633